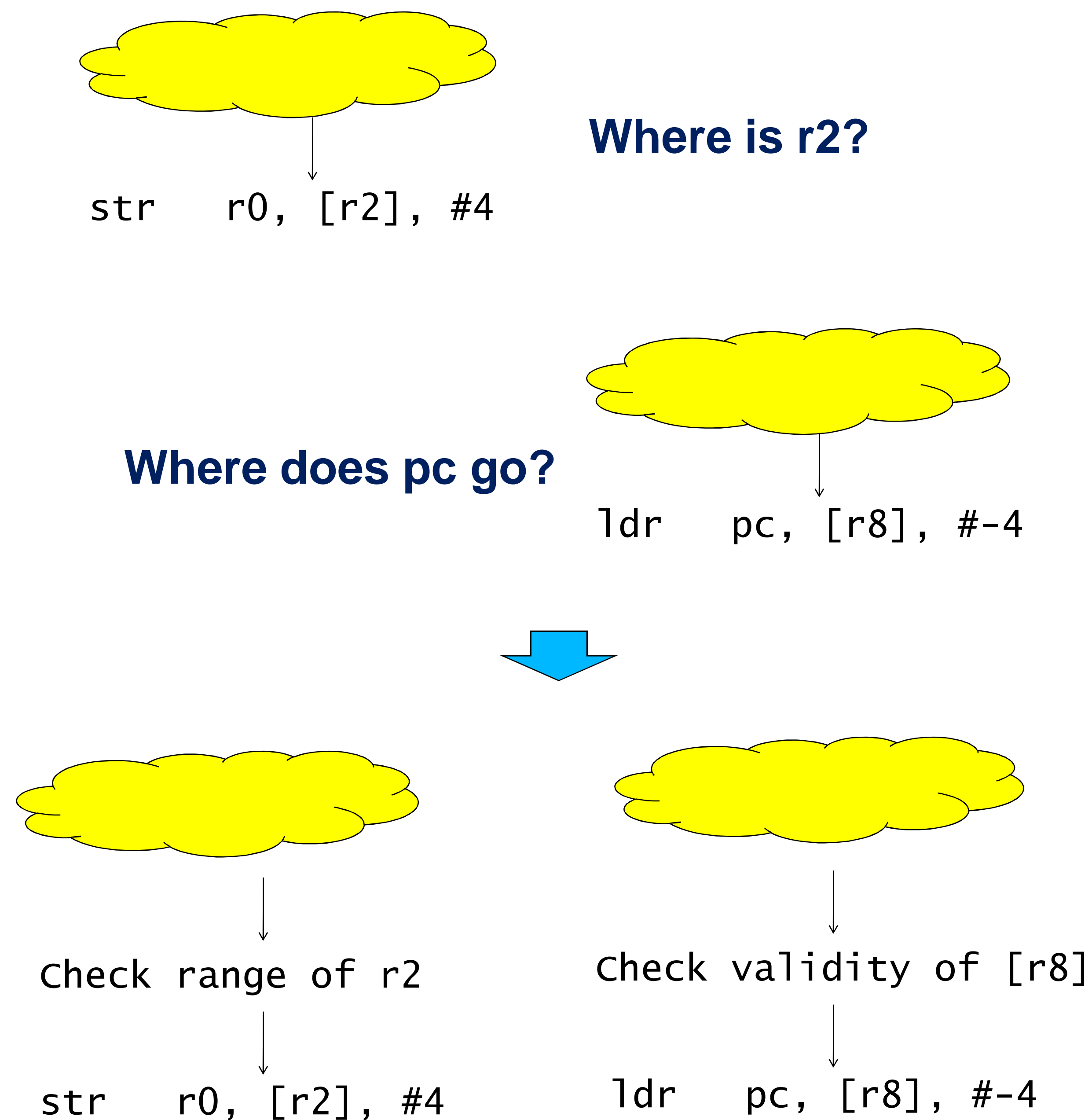


# Comparing Program Logics for Reasoning about Safety Properties

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## Background



To prove memory writes and control transfers do not interfere with other programs in embedded systems.

Need a program logic with properties:

- Scaling to thousands of lines of machine code
- Capable of reasoning code with arbitrary control flow transfers
- Handling infinite loops

## Hoare Logic with Separation Conjunction

Theorem is:

$$\vdash \text{PROG\_SPEC} \left( \begin{array}{l} \text{Precondition} \\ \{ \text{Code} \} \\ \text{Postcondition} * \text{Property} \end{array} \right)$$

✗ Almost impossible to write a sensible Hoare triple for thousands of lines of code.

✗ Extremely difficult to handle indirect control flow transfers.

✗ Unable to handle infinite loops.

## Hoare Logic with Label Predicate

Theorem is:

$$\vdash \text{PROG\_SPEC} \left( \begin{array}{l} \text{Precondition} \text{ UNION } \{ (l, p) \} \\ \{ \text{Code} \} \\ \text{Postcondition} \text{ UNION } \{ (k, q) \} * \text{Property} \end{array} \right)$$

- ✓ Enable flexible composition of specifications by matching labels.
- ✗ Composition may become intractable.
- ✗ Fail to handle infinite loops.

## Hoare Logic and Condition Verification

The specification of a basic block is:

$$\vdash \text{SAFE\_BBL} \left( \begin{array}{l} \text{Precondition} \\ \{ \text{Code} \} \\ \text{Postcondition} * \text{Property} \end{array} \right)$$

The specification of a program is:

$$\vdash \text{SAFE\_PROGRAM} \text{ guard prog predecessor} =$$

$$\forall \text{ bbl} \in \text{prog}. \text{SAFE\_BBL } \text{bbl} \wedge$$

$$\forall \text{ pre} \in (\text{predecessor } \text{bbl}).$$

$$\forall \text{ branch} \in (\text{post\_condition\_of } \text{pre}).$$

$$\text{branch jumps\_to } \text{bbl} \Rightarrow \text{guard } \text{bbl}$$

- ✓ Enable modular reasoning in the unit of basic blocks.
- ✓ Handle arbitrary control flow transfers.
- ✓ Handle infinite loops.

## References

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